



1800 René-Lévesque Blvd. W.
Montréal, Québec
H3H 2H2

T 514.906.2447
F 514.906.2500
www.hyperchip.com

For Immediate Release

Hyperchip Unveils High-Capacity Core Router Based on Revolutionary ASIC Design

MONTREAL - May 30, 2001 - Hyperchip Inc., The Petabit Routing Company™, is unveiling a carrier-class core router next week at SUPERCOMM 2001 that offers unprecedented performance and operating advantages to network operators.

Using a revolutionary ASIC architecture based on massively parallel semiconductors, Hyperchip's PBR-1280 is capable of delivering unmatched scalability. Effective for applications ranging from gigabits to petabits-per-second, Hyperchip's PBR-1280 is the first MPLS-centric router designed for building efficient and cost effective IP networks.

"Hyperchip is leading the way for deployment of high performance networks that allow carriers to manage costs and increase revenue velocity, while simplifying capacity planning and life cycle management," said Brian Barry, Hyperchip's CEO. "Hyperchip's PBR-1280 incorporates the flexibility, reliability and rapid service provisioning required by network operators to reduce their capital and operational expenses."

Displayed for the first time outside Hyperchip's labs at SUPERCOMM 2001, the PBR-1280 is based on a combination of Hyperchip's scalable software and its Hyperchip Matrix™, a unique, non-blocking, intelligent switch fabric with massive scalability that offers the most cost-effective evolution strategy in the industry. The Hyperchip Matrix™ offers fractional microsecond latency and controls routing bottlenecks by ensuring balanced traffic distribution throughout the system, thus providing maximum resiliency. The intelligent switch fabric supports hundreds of concurrent operations per ASIC, providing a true non-blocking routing solution with unparalleled scalability.

- more -

“The product available for customer lab trials this fall delivers the technology network operators need to offer high-yield, revenue-generating services in a system that can match the bandwidth demand curve for years to come,” added Barry.

“Currently, the telecommunication market is consolidating, with fewer but larger players expected to emerge. As networks continue to grow, MAN and WAN architectures need a scalable, modular routing platform to meet this ever-increasing bandwidth demand. Hyperchip's PBR-1280 is positioned to take advantage of this need, ” said Muayyad Al-Chalabi, Director Core Switching & Routing, RHK.

Hyperchip at SUPERCOMM 2001

Hyperchip will demonstrate its PBR-1280 using the latest OC-192 Agilent RouterTester test systems at booth 4569, Hall E, at the SUPERCOMM 2001 conference in Atlanta, June 5-7, 2001.

About Hyperchip

Based in Montreal, Hyperchip is the leader in petabit scalability for the WAN, and is driving the merger of the world's networks to provide end-to-end multi-gigabit connections between users, servers and storage providers - regardless of distance. Founded in 1997 after 10 years of patent work and four years of proof-of-concept work, Hyperchip has pioneered major advances in key computing and communication technologies. Hyperchip's technology partners include IBM, ARC Cores and Virata.

Hyperchip has raised more than (US) \$100 million in venture financing from leading telecom investors, including Siemens Mustang Ventures, Argo Global Capital, Vertex Technology Fund, Advent International, TechnoCap, Optical Capital Group, Altamira, Amerindo, Artemis, Enron Broadband Services, JT Ventures, Morgan Stanley Dean Witter, Pilgrim Baxter, Putnam Investments, and Van Wagoner Capital Management. For more information about Hyperchip, visit the company Web site at www.hyperchip.com.

- 30 -

For more information, please contact:

Alain Bourget
Hyperchip Inc.
514-906-2489
abourget@hyperchip.com

Jennifer Goldfinch
Hyperchip Inc.
514-906-4844
jgoldfinch@hyperchip.com